SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply and Output						

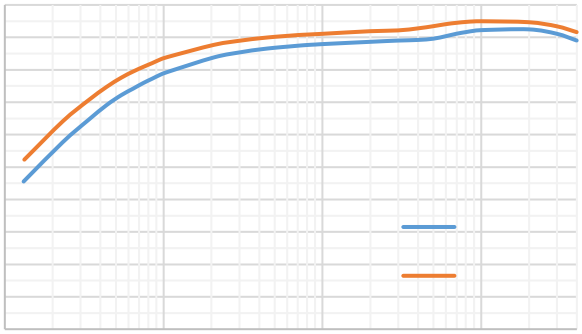


Figure 1. Efficiency vs Load Current(VIN=12V)

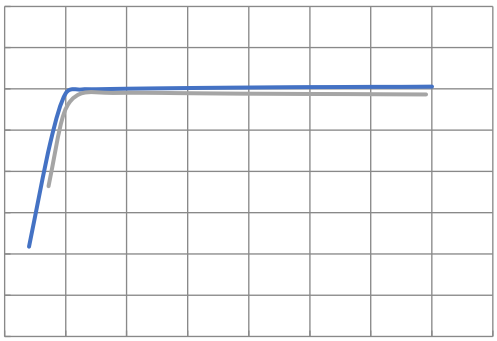


Figure 2. Line Regulation

Figure 3. Load Regulation

Figure 4. FB Voltage Vs. Temperature

Figure 5. UVLO Vs. Temperature

Figure 6. Quiescent Current Vs. Temperature

Adaptive On-time Control

Power Saving Mode (PSM)

Under Voltage Lockout UVLO

Bootstrap Voltage Regulator

Thermal Shutdown

°

°

Typical Application

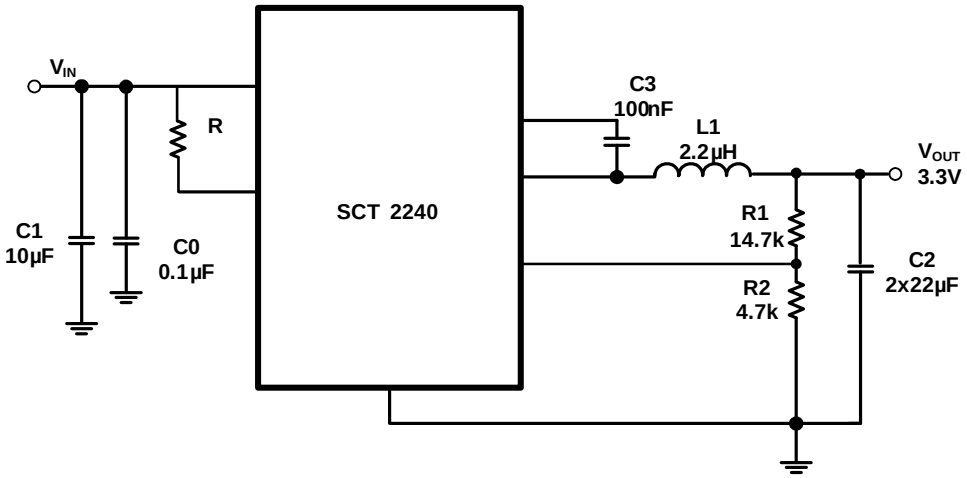


Figure 8. 12V Input, 3.3V/4A Output

Design Parameters

Design Parameters	Example Value

Input Capacitor Selection

Thermal Considerations

NOTE: